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Enhanced support for selected Power Architecture™ derivatives from Freescale and STMicroelectronics:

Universal Debug Engine 2.4.12. supports program trace using the Nexus interface

Lauta (Germany), November 2008 - pls Programmierbare Logik & Systeme presents - at electronica 2008 in Area A6, Booth S02 - the Universal Debug Engine (UDE) 2.4.12., which enables program trace via the Nexus interface. This function is currently available - in combination with the Universal Access Device 2+ from pls - for the Power Architecture™ derivatives MPC55xx and MPC56xx from Freescale as well as SPC56xx from STMicroelectronics.

A 2-bit, 4-bit or, with the MCUs of the MPC55xx family, even 12-bit wide trace data port with up to 180 MHz clock frequency are supported, whereby up to 1 megasamples can be recorded. With a compression of the trace data direct by the trace hardware, this represents a multiple of machine commands. Moreover, every sample can contain eight additional external hardware signals.

Recording of the samples takes place synchronously to the Nexus clock frequency. This enables an optimal use of the trace memory and application optimized timestamps. Start and stop of the recording can be comfortably controlled via pre-trigger, mid-trigger, post-trigger or address-trigger.

The trace window in the user interface offers the developer a direct link from the trace samples to the associated source code, the display of the program runtime on the basis of the timestamps as well as extensive search functions.

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pls Programmierbare Logik & Systeme GmbH

pls Programierbare Logik & Systeme GmbH, located in Lauta, Germany, was founded in 1990 by Thomas Bauch and Dr. Stefan Weisse. The company is among the worldwide leading suppliers of software debugging solutions and complete development tools for the 16-bit and 32-bit microcontroller families from Infineon Technologies and STMicroelectronics as well as a number of different ARM7/ARM9 derivatives. The innovative software architecture of the Universal Debug Engine (UDE) guarantees optimal conditions for the debugging of modern System-on-Chip (SoC) based systems. The complementary equipment family Universal Access Device (UAD), with transfer rates of up to 3.5 Mbytes per second, opens up a fully new dimension in data download speeds. Additional information can be found at: www.pls-mc.com

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