Avnet Silica Opens Registration for European MiniZed SpeedWay Design Workshop Series

Training gives engineers head start in mastering design flows using Xilinx's Vivado Design Suite

Poing/Germany, 18th January 2018 – Avnet Silica, an Avnet company (NYSE: AVT), today announced that registration is now open for a series of interactive SpeedWay Design Workshops™ to help engineers jump-start the development of single-core Xilinx® Zynq®-7000 All Programmable SoC devices using the Avnet MiniZed™ Zynq SoC development kit, a cost-optimized prototyping platform for embedded vision and Industrial IoT systems. These one-day instructional programs, hosted by Avnet Silica across Europe, will train customers on the latest design flows using Xilinx's Vivado® Design Suite through a mix of lecture and interactive labs.

Avnet's MiniZed Zynq SoC development kit showcases the power of the single-core Xilinx® Zynq®-7000 All Programmable SoC devices, which feature a single-core ARM® Cortex™-A9 processor paired with 28nm Artix®-7 based programmable logic. MiniZed features on-board connectivity through USB, Wi-Fi and Bluetooth and a compact design that is small enough to fit into a shirt pocket. Peripherals can be plugged into dual Pmod-compatible connectors, the Arduino-compatible shield interface or the USB 2.0 host interface. Workshop attendees will have the opportunity to integrate the HTU21D peripheral module from TE Connectivity (TE), a global leader in connectivity and sensor solutions, and explore the results through the interactive lab sessions.

For designers new to Xilinx SoC technology and tools, or those looking to brush up their skills, Avnet Silica will offer two introductory courses: Developing Zynq Hardware and Developing Zynq Software. More experienced designers can choose from two advanced course offerings: Integrating Sensors on MiniZed with PetaLinux and A Practical Guide to Getting Started with Xilinx SDSoC™ development environment. A brief overview of the SpeedWay Workshop series can be viewed at www.avnet-silica.com/speedways.

"Avnet's MiniZed SpeedWay Design Workshops teach developers the basics of both hardware and software integration using the MiniZed platform," said Jim Beneke, vice president, engineering and technology, Avnet. "With this training, engineers will have the technology, tools and design expertise they need to more quickly capture the benefits of the single-core Xilinx Zynq-7000 All Programmable SoC, which is one of the industry's most flexible, scalable and secure platforms for cost-sensitive as well as high-performance applications."

For more details about the SpeedWay Design Workshops™ as well as dates, locations and registrations visit **www.avnet-silica.com/speedways**

Visit the www.minized.org site for MiniZed development kit tutorials and to join the MiniZed community where users can collaborate with other engineers also working on Zynq designs.

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About Avnet Silica

Avnet Silica is the European semiconductor specialist division of Avnet, one of the leading global technology distributors, and acts as the smart connection between customers and suppliers. The distributor simplifies complexity by providing creative solutions, technology and logistics support. Avnet Silica is a partner of leading semiconductor manufacturers and innovative solution providers over many years. With a team of more than 200 application engineers and technical specialists, Avnet Silica supports projects all the way from the idea to the concept to production. For more information, visit www.avnet-silica.com

About Avnet

From idea to design and from prototype to production, Avnet supports customers at each stage of a product's lifecycle. A comprehensive portfolio of design and supply chain services makes Avnet the go-to guide for innovators who set the pace for technological change. For nearly a century, Avnet has helped its customers and suppliers around the world realize the transformative possibilities of technology. Learn more about Avnet at: www.avnet.com.

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