

PRESS RELEASE

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PLS' UDE 4.4.5 provides optimized testing and debugging environment for Infineon's XMC4700/XMC4800 SoCs

Lauta (Germany) – June 30, 2015 – PLS Programmierbare Logik & Systeme presents Version 4.4.5 of its Universal Debug Engine (UDE). The UDE 4.4.5 provides developers with an optimized testing and debugging environment for Infineon's new XMC4700/XMC4800 microcontrollers.

The 32-bit SoCs, which are based on an ARM® Cortex®-M4 processor with Floating Point Unit (FPU), were specifically developed for use in industrial applications. The new microcontrollers feature a comprehensive range of peripheral functions such as an EtherCAT® (Ethernet for Control Automation Technology) communication unit (XMC4800 series) and six CAN nodes. PLS' UDE 4.4.5 fully supports both the peripheral units and the internal debug resources of the new high-performance microcontroller family. The integrated FLASH/OTP programming functionality of the UDE guarantees maximum speeds in the whole Delete-Download-Programming-Verify cycle.

The various possibilities for graphically displaying variables and their links to physical values within the Universal Debug Engine benefit above all from the real-time properties of the XMC4700/XMC4800 family. For example, it is possible for the debugger to read and write the entire main memory whilst a program is running without impairing real-time behavior. This permits the animation of variables, registers and memory content at runtime. In addition, the periodic recording of the instruction counter permits a profiling function with portrayal of the percentage share of functions in the application's runtime. The integrated units for program, data and performance trace are, of course, also fully supported.

Furthermore, in combination with PLS' Universal Access Device 2 (UAD2) family, developers can use the built-in Bootstrap Loader (BSL) implemented in the Infineon XMC4700/XMC4800 SoCs to program the on-chip flash memory via an asynchronous serial interface or the CAN bus. A digitally isolated connection to the target is also optionally possible with the UAD family.

If required, a full Eclipse integration via an own debug perspective with complete cross-debugger functionality is also available.

PLS' UDE 4.4.5 is available now. According to Infineon, samples of the XMC4700/XMC4800 devices will be available in August 2015.

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PLS Programmierbare Logik & Systeme GmbH

PLS Programmierbare Logik & Systeme GmbH, based in Lauta, Germany, was founded in 1990. With its innovative modular test and development tools, the company has demonstrated for over two decades its position as an international technology leader in the field of debuggers, emulators and trace solutions for 16-bit and 32-bit microcontrollers. The software architecture of the Universal Debug Engine (UDE) guarantees optimal conditions for debugging SoC-based systems. For example, by means of the intelligent use of modern on-chip debugging and on-chip trace units, valuable functions such as profiling and code coverage are available for the system optimization. Furthermore, the associated Universal Access Device (UAD2/UAD3+) product family, with transfer rates of up to 3.5 MBytes/s and a wide range of interfaces, offers entirely new dimensions for fast and flexible access to multicore systems. Important architectures such as TriCore, Power Architecture, XC2000/XE166, ARM, Cortex, SH-2A, XScale and C166/ST10 as well as simulation platforms of different vendors are supported. For further information about the company, please visit www.pls-mc.com.

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